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CALIF. S. N. CODES

	Class	Subject
ISSUE CLASSIFICATION		

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PATENT NUMBER

U.S. UTILITY Patent Application

AS SCANNED SA ③ Q.A. A.G.	O.I.P.E.	PATENT DATE
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APPLICATION NO. 09/827056	CONT/PRIOR D	CLASS 257 438	SUBCLASS 283	ART UNIT 2811	EXAMINER <i>[Signature]</i>
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APPLICANTS

Chau-Mai Li
Kung-Yen Su
Albert Knudsen
Ping Guo

Magee.

TITLE

Method of fabricating high-coupling ratio split gate flash memory cell array

PTO-2040
12/89

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <div style="text-align: center;">5</div>	Figs. Drwg. <div style="text-align: center;">11</div>	Print Fig. <div style="text-align: center;">24</div>	Total Claims <div style="text-align: center;">15</div>	Print Claim for O.G. <div style="text-align: center;">1</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div style="display: flex; justify-content: space-between;"> <i>Kenneth W. Hayes</i> 10/9/03 </div> <div style="display: flex; justify-content: space-between;"> (Assistant Examiner) (Date) </div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____	<div style="display: flex; justify-content: space-between;"> _____ _____ </div> <div style="display: flex; justify-content: space-between;"> (Primary Examiner) (Date) </div>			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	<div style="display: flex; justify-content: space-between;"> _____ _____ </div> <div style="display: flex; justify-content: space-between;"> (Legal Instruments Examiner) (Date) </div>			ISSUE BATCH NUMBER	
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